FIG.1

1 FRAME

BASE STATION REFERENCE FRAME

DELAY PROFILE GENERATION TIMING

5 CH #0 CM PATTERN (0: DATA RECEIVED, 1: GAP)

FIG.2

102 RADIO SECTION

103 TRANSMISSION-OFF PERIOD DETECTOR

10 104 DELAY PROFILE GENERATION CONTROLLER

105 DELAY PROFILE GENERATOR

106 PATH SEARCHER

RECEIVED PATH TIMING INFORMATION

FROM BASEBAND CONTROLLER

15

FIG.3

START

ST201 DETECT TRANSMISSION-OFF PERIOD

ST202 DETERMINE CHANNEL USE STATE

20 ST203 OVERLAP WITH TRANSMISSION-OFF PERIOD?

ST204 REARRANGE

END

FIG.4, FIG.5

25 図1参照

FIG.6

504 RECEIVED BASEBAND SIGNAL SWITCH

FIG.7

FROM RADIO SECTION 502-1

5 FROM RADIO SECTION 502-N

FROM DELAY PROFILE GENERATION CONTROLLER 104

TO DELAY PROFILE GENERATOR 105

FIG.8

10 CELL #702

FIG.9

図1,4,5参照

SECTOR

15

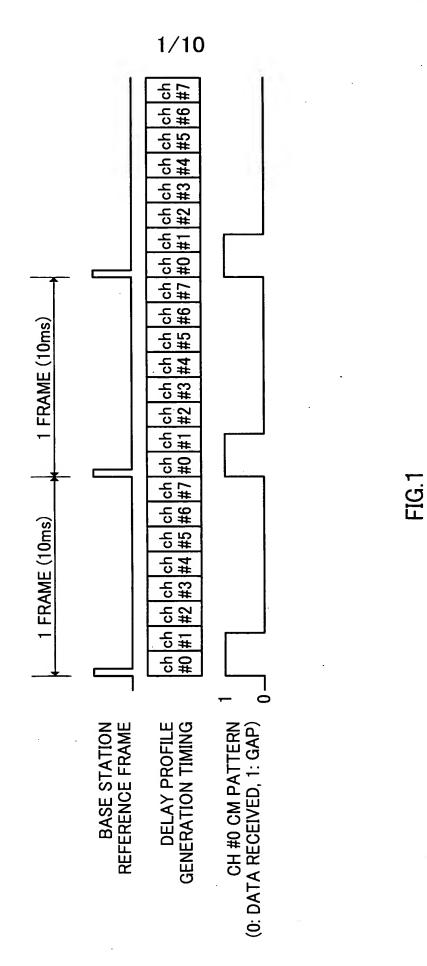
FIG.10

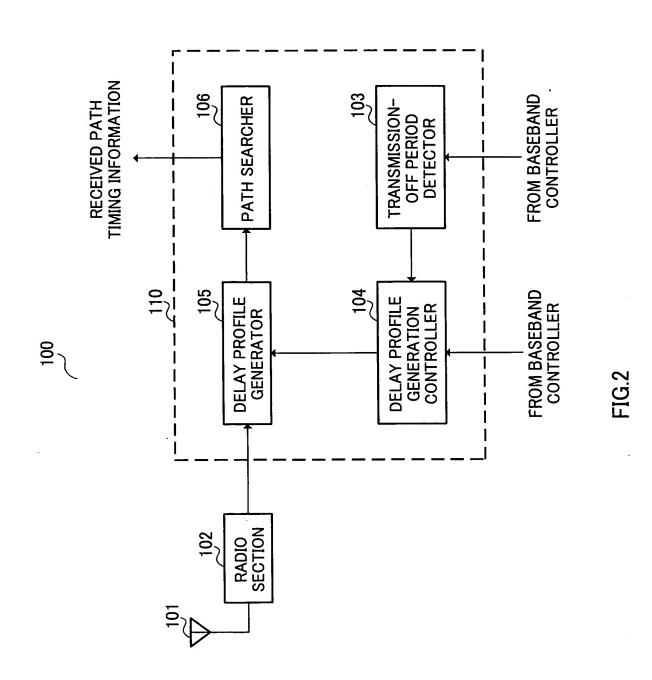
SECTOR

FRAME

20 FIG.11

図1,4,5,9参照





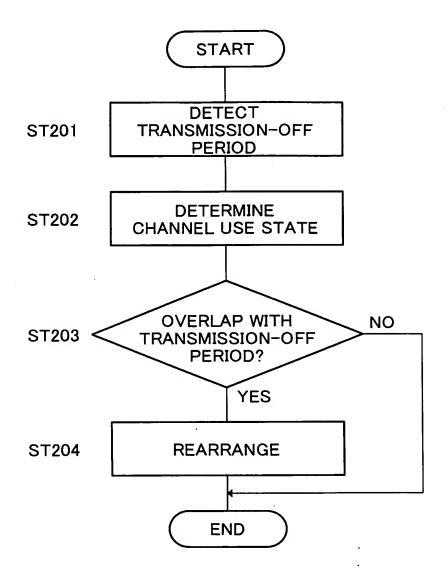


FIG.3

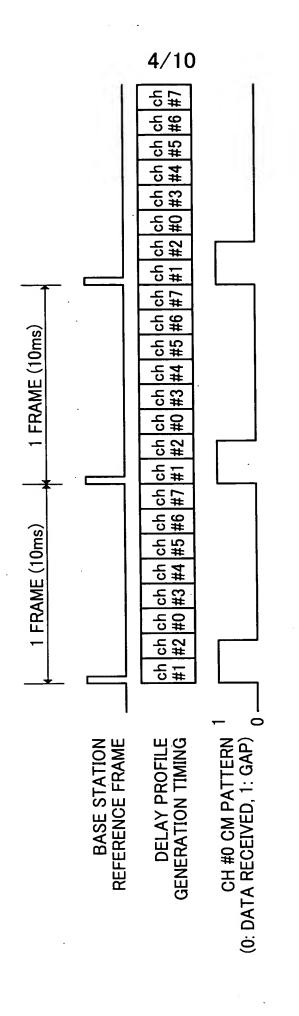
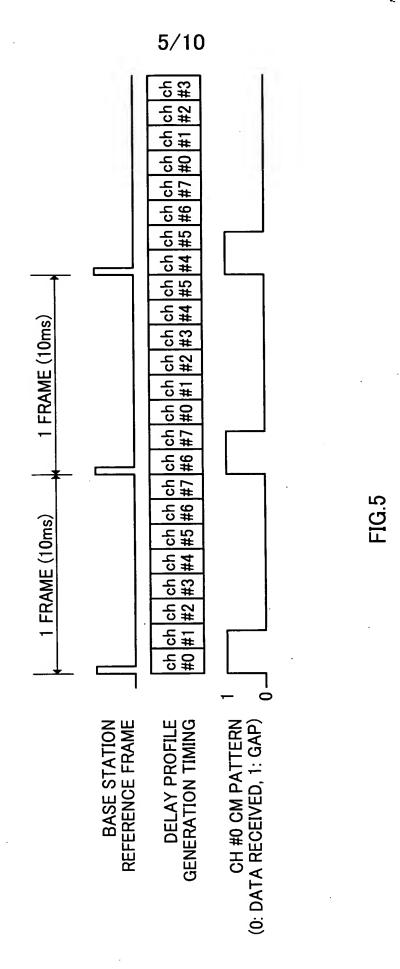
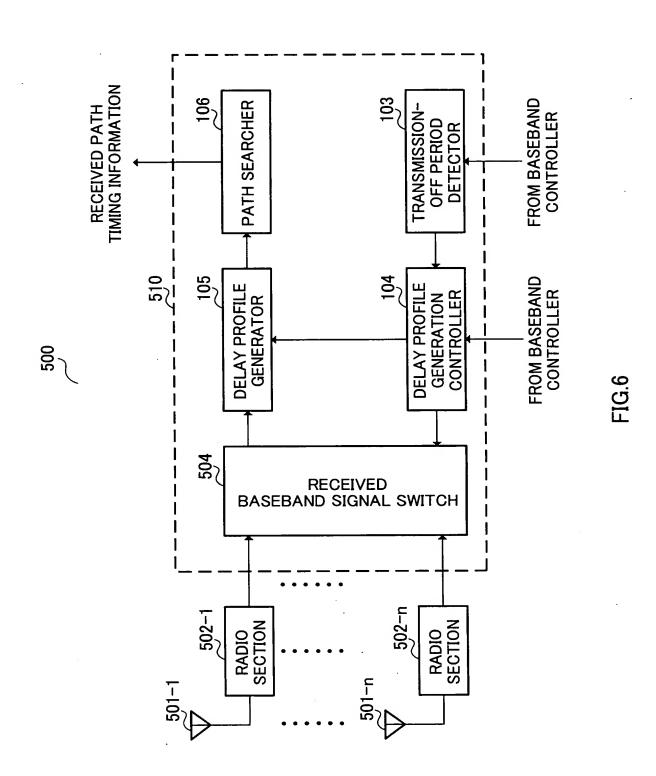


FIG.4





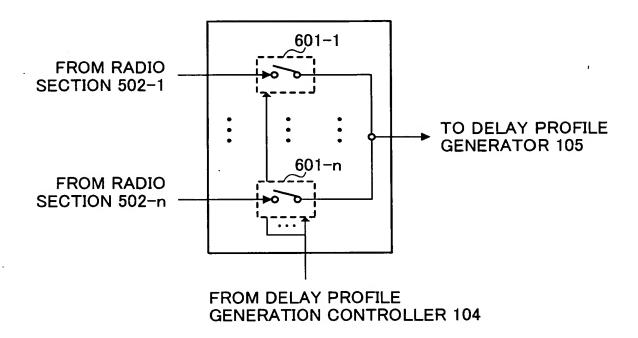


FIG.7

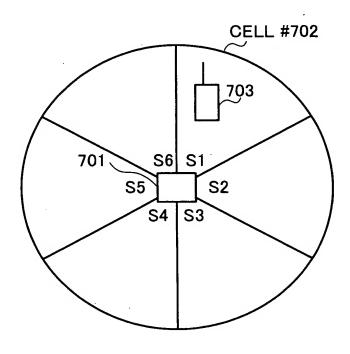
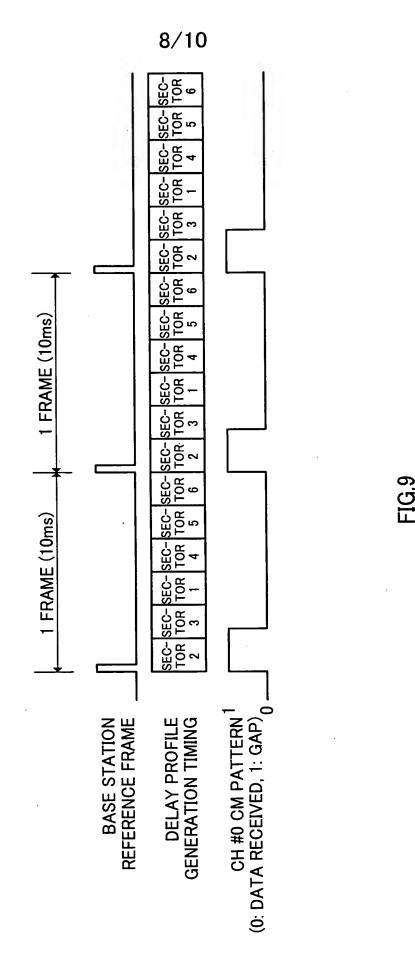


FIG.8



1 FRAME (10ms)	SECTOR 6
	SECTOR 5
	SECTOR 4
	SECTOR 3
	SECTOR 2
	Ch7 Ch6 C ch5 Ch4 Ch3 Ch2 Ch1 Ch0

FIG. 10

